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(Original Signature of Member)

117TH CONGRESS
1ST SESSION

H. R. _____

To provide for a comprehensive and integrative program to accelerate microelectronics research and development at the Department of Energy, and for other purposes.

IN THE HOUSE OF REPRESENTATIVES

Mr. Tonko introduced the following bill; which was referred to the Committee
on _____

A BILL

To provide for a comprehensive and integrative program to accelerate microelectronics research and development at the Department of Energy, and for other purposes.

1 *Be it enacted by the Senate and House of Representa-*
2 *tives of the United States of America in Congress assembled,*

3 **SECTION 1. SHORT TITLE.**

4 This Act may be cited as the “Microelectronics Re-
5 search for Energy Innovation Act” or the “Micro Act”.

6 **SEC. 2. DEFINITIONS.**

7 In this Act:

1 (1) DEPARTMENT.—The term “Department”
2 means the Department of Energy.

3 (2) HISTORICALLY BLACK COLLEGE AND UNI-
4 VERSITY.—The term “historically Black college and
5 university” has the meaning given the term “part B
6 institution” in section 322 of the Higher Education
7 Act of 1965 (20 U.S.C. 1061).

8 (3) INSTITUTION OF HIGHER EDUCATION.—The
9 term “institution of higher education” has the
10 meaning given the term in section 101(a) of the
11 Higher Education Act of 1965 (20 U.S.C. 1001(a)).

12 (4) MINORITY SERVING INSTITUTION.—The
13 term “minority serving institution” includes the en-
14 tities described in any of the paragraphs (1) through
15 (7) of section 371(a) of the Higher Education Act
16 of 1965 (20 U.S.C. 1067q(a)).

17 (5) NATIONAL LABORATORY.—The term “Na-
18 tional Laboratory” has the meaning given the term
19 in section 2 of the Energy Policy Act of 2005 (42
20 U.S.C. 15801).

21 (6) SECRETARY.—The term “Secretary” means
22 the Secretary of Energy.

23 (7) TRIBAL COLLEGE AND UNIVERSITY.—The
24 term “Tribal College and University” has the mean-

1 ing given in section 316 of the Higher Education
2 Act of 1965 (20 U.S.C. 1059c).

3 **SEC. 3. FINDINGS.**

4 Congress finds that—

5 (1) the coming end of Moore’s Law presents
6 major technological challenges and opportunities for
7 the United States and important implications for na-
8 tional security, economic competitiveness, and sci-
9 entific discovery;

10 (2) future progress and innovation in microelec-
11 tronics, and maintaining a robust domestic micro-
12 electronics supply chain, will require an approach
13 that advances relevant materials science, electronic
14 and photonic device technologies, processing and
15 packaging technologies, manufacturing technologies,
16 circuit, chip, and system architecture, and software
17 system and algorithm development in a co-design
18 fashion;

19 (3) the National Laboratories possess unique
20 technical expertise and user facilities that are essen-
21 tial to overcoming foundational research challenges
22 relevant to the topics described in paragraph (2),
23 and translating and transferring research outcomes
24 to industry; and

1 (4) the assets described in paragraph (3) will
2 enable the Department to drive advances in micro-
3 electronics that are essential to meeting future needs
4 in areas critical to its mission as well as the future
5 competitiveness of the domestic microelectronics in-
6 dustry, including high-performance computing,
7 emerging data-centric computing approaches, and
8 energy-efficient computing; optical sensors, sources,
9 and wireless networks; and power electronics and
10 electricity delivery systems.

11 **SEC. 4. MICROELECTRONICS RESEARCH PROGRAM.**

12 (a) IN GENERAL.—The Secretary shall carry out a
13 cross-cutting program of research, development, and dem-
14 onstration of microelectronics relevant to the mission of
15 the Department and in the service of the nation’s global
16 competitiveness in the field of microelectronics.

17 (b) RESEARCH AREAS.—In carrying out the program
18 under subsection (a), the Secretary shall award financial
19 assistance to eligible entities under subsection (c) to carry
20 out research projects in—

21 (1) foundational science areas, including—

22 (A) materials sciences, chemical sciences,
23 and plasma science synthesis, and fabrication;

24 (B) novel microelectronics devices, includ-
25 ing emerging memory and storage technologies;

1 (C) diverse computing architectures and
2 paradigms, including analog computing and
3 edge computing;

4 (D) data-driven modeling and simulation;

5 (E) integrated sensing, power harvesting,
6 and communications;

7 (F) component integration and subsystems;

8 (G) photonic integration; and

9 (H) development of co-design frameworks
10 for all stages of microelectronics design, devel-
11 opment, fabrication, and application;

12 (2) methods for leveraging advanced simulation,
13 artificial intelligence, and machine learning to en-
14 hance co-design and discovery in microelectronics;

15 (3) in consultation with the National Institute
16 of Standards and Technology, fabrication and proc-
17 essing science and metrology associated with micro-
18 electronics manufacturing, including lithography,
19 patterning, surface deposition, etching, and cleaning;

20 (4) approaches for optimizing system-level ad-
21 vanced computing energy efficiency for the electrical
22 grid, power electronics, and other energy infrastruc-
23 ture;

24 (5) approaches for enhancing the durability and
25 lifetime of radiation-hardened electronics; and

1 (6) enhancement of microelectronics security,
2 including the development of integrated devices,
3 packages, and thermal management for severe envi-
4 ronments and national security.

5 (c) ELIGIBLE ENTITIES.—The entities eligible to re-
6 ceive financial assistance under this section include—

7 (1) an institution of higher education, including
8 historically Black colleges and universities, Tribal
9 colleges and universities, and minority serving insti-
10 tutions;

11 (2) a nonprofit research organization;

12 (3) a State research agency;

13 (4) a National Laboratory;

14 (5) a private commercial entity;

15 (6) a partnership or consortium of 2 or more
16 entities described in paragraphs (1) through (5); and

17 (7) any other entities the Secretary deems ap-
18 propriate.

19 (d) TECHNOLOGY TRANSFER.—In carrying out the
20 program described in subsection (a), the Secretary, in co-
21 ordination with the Director of the Office of Technology
22 Transitions shall support translational research and trans-
23 fer of microelectronics technologies for the benefit of
24 United States economic competitiveness.

1 (e) EDUCATION AND OUTREACH.—In carrying out
2 the program under subsection (a), the Secretary shall sup-
3 port education and outreach activities to disseminate in-
4 formation and promote public understanding of microelec-
5 tronics and the microelectronics workforce.

6 (f) REPORT.—Not less than 180 days after the enact-
7 ment of this Act, the Secretary shall submit to the Com-
8 mittee on Science, Space, and Technology of the House
9 of Representatives, and the Committee on Energy and
10 Natural Resources of the Senate, a report describing the
11 goals, priorities, and anticipated outcomes of the program
12 described in subsection (a).

13 (g) FUNDING.—There are authorized to be appro-
14 priated to the Secretary to carry out the activities de-
15 scribed in this section—

- 16 (1) \$75,000,000 for fiscal year 2022;
- 17 (2) \$100,000,000 for fiscal year 2023;
- 18 (3) \$100,000,000 for fiscal year 2024;
- 19 (4) \$100,000,000 for fiscal year 2025; and
- 20 (5) \$100,000,000 for fiscal year 2026.

21 **SEC. 5. MICROELECTRONICS SCIENCE RESEARCH CEN-**
22 **TERS.**

23 (a) IN GENERAL.—In carrying out the program
24 under section 4, the Secretary, acting through the Direc-
25 tor of the Office of Science, shall establish up to four

1 Microelectronics Science Research Centers (referred to in
2 this section as “Centers”) to conduct mission driven re-
3 search to address foundational challenges in the design,
4 development, and fabrication of microelectronics and to fa-
5 cilitate the translation of research results to industry.

6 (b) ACTIVITIES.—The activities of the Centers au-
7 thorized under this section shall include research, develop-
8 ment, and demonstration activities for—

9 (1) accelerating the development of new micro-
10 electronics science and technology, including mate-
11 rials, devices, circuits, systems, architectures, fab-
12 rication tools, processes, diagnostics, modeling, syn-
13 thesis, and metrology;

14 (2) advancing the sustainability and energy effi-
15 ciency of new microelectronics devices, packages, and
16 systems;

17 (3) application-driven co-design and prototyping
18 of novel devices to facilitate laboratory-to-fabrication
19 transition;

20 (4) advancing knowledge and experimental ca-
21 pabilities in surface and materials science, plasma
22 science, and computational and theoretical methods,
23 including artificial intelligence, machine learning,
24 multi-scale co-design, and advanced supercomputing

1 capabilities to invent and manufacture revolutionary
2 microelectronic devices;

3 (5) creating technology testbeds for prototyping
4 platforms for validation and verification of new ca-
5 pabilities and sharing of ideas, intellectual property,
6 and the unique facilities of the Department; and

7 (6) supporting long-term and short-term work-
8 force development in microelectronics.

9 (c) REQUIREMENTS.—

10 (1) SELECTION AND DURATION.—The Director
11 of the Office of Science shall select Centers on a
12 competitive, merit-reviewed basis for a period of not
13 more than 5 years, subject to the availability of ap-
14 propriations, beginning on the date of establishment
15 of that Center.

16 (2) APPLICATIONS.—An eligible applicant under
17 this subsection shall submit to the Director of the
18 Office of Science an application at such time, in
19 such manner, and containing such information as
20 the Director deems appropriate.

21 (3) ELIGIBLE APPLICANTS.—The Director of
22 the Office of Science shall consider applications from
23 National Laboratories, institutions of higher edu-
24 cation, research centers, or a consortia thereof, or

1 any other entity that the Secretary of Energy deems
2 appropriate.

3 (4) RENEWAL.—After the end of either period
4 described in paragraph (1), the Director of the Of-
5 fice of Science may renew support for the Center for
6 a period of not more than 5 years on a merit-re-
7 viewed basis. For a Center in operation for 10 years
8 after its previous selection on a competitive, merit-
9 reviewed basis, the Director may renew support for
10 the center on a competitive, merit-reviewed basis for
11 a period of not more than 5 years, and may subse-
12 quently provide an additional renewal on a merit-re-
13 viewed basis for a period of not more than 5 years.

14 (5) TERMINATION.—Consistent with the exist-
15 ing authorities of the Department, the Director of
16 the Office of Science may terminate an underper-
17 forming center for cause during the performance pe-
18 riod.

19 (d) TECHNOLOGY TRANSFER.—The Director of the
20 Office of Science, in coordination with the Director of the
21 Office of Technology Transitions, shall implement part-
22 nerships with industry groups for the purpose of facili-
23 tating the translation and transfer of research results pro-
24 duced by the Centers.

25 (e) COORDINATION.—The Secretary shall—

1 (1) establish a coordinating network to coordi-
2 nate cross-cutting research and foster communica-
3 tion and collaboration among the Centers; and

4 (2) ensure the coordination, and avoid unneces-
5 sary duplication, of the activities of each Center with
6 the activities of—

7 (A) other research entities of the Depart-
8 ment, including—

9 (i) the Nanoscale Science Research
10 Centers;

11 (ii) the National Quantum Informa-
12 tion Science Research Centers;

13 (iii) the Energy Frontier Research
14 Centers;

15 (iv) the Energy Innovation Hubs; and

16 (v) the National Laboratories;

17 (B) the National Semiconductor Tech-
18 nology Center authorized in title XCIX of divi-
19 sion H of the William M. (Mac) Thornberry
20 National Defense Authorization Act for Fiscal
21 Year 2021 (Public Law 116–283);

22 (C) institutions of higher education;

23 (D) industry; and

24 (E) research activities carried out by other
25 Federal agencies.

1 (f) FUNDING.—The Secretary of Energy shall allo-
2 cate up to \$25,000,000 for each Center established under
3 this section for each of fiscal years 2022 through 2026,
4 subject to the availability of appropriations.